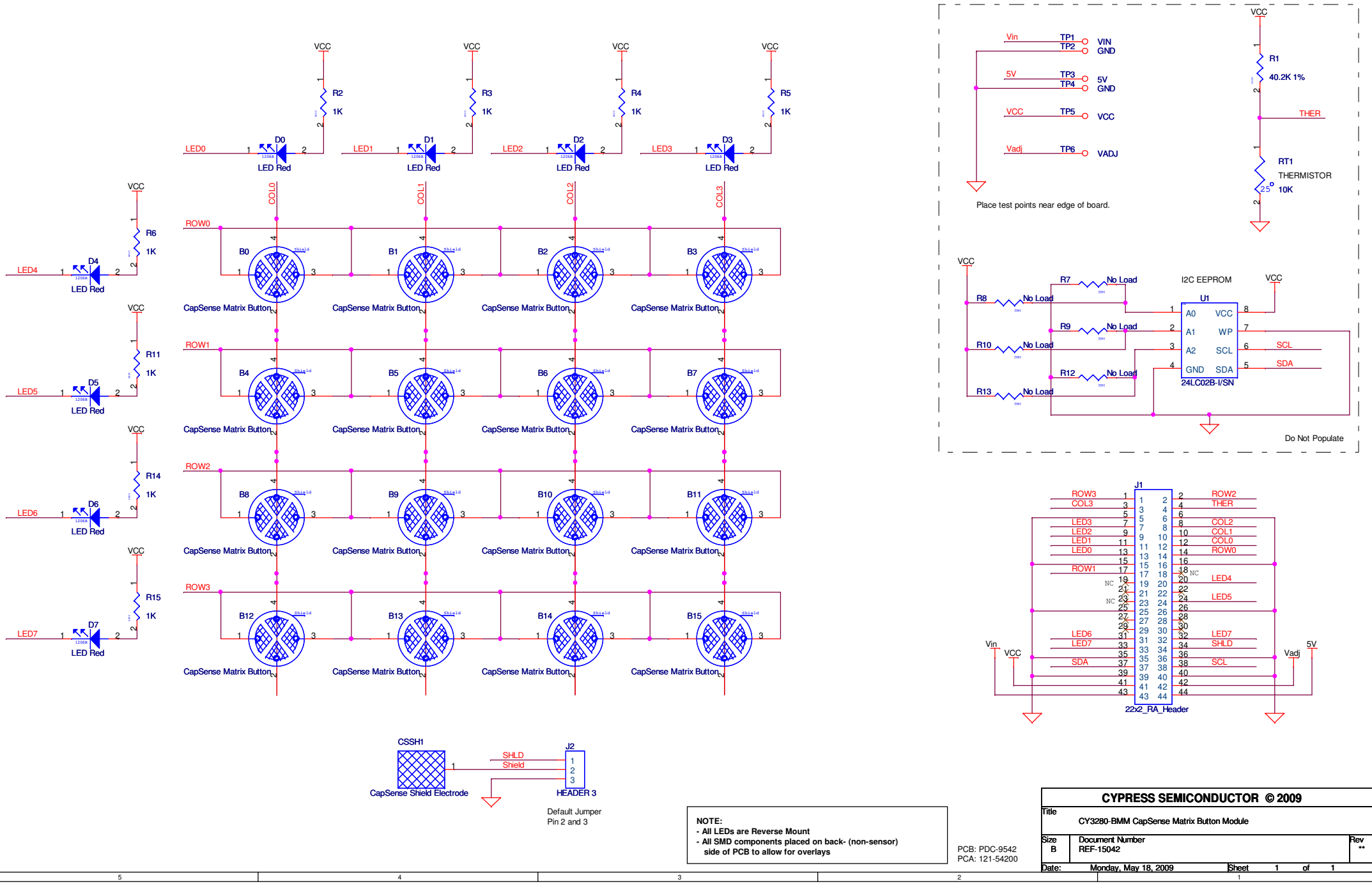
VCC
R2
1K
R3
R4
R5
LED0
LED1
LED2
LED3
D0
D1
D2
D3
LED Red
COL0
COL1
COL2
COL3
ROW0
ROW1
ROW2
ROW3
B0
B1
B2
B3
B4
B5
B6
B7
B8
B9
B10
B11
B12
B13
B14
B15
CapSense Matrix Button
R6
R11
R14
R15
LED4
LED5
LED6
LED7
D4
D5
D6
D7
Vin
5V
VCC
Vadj
TP1
TP2
TP3
TP4
TP5
TP6
VIN
GND
VADJ
Place test points near edge of board.
R1
40.2K 1%
THER
RT1
THERMISTOR
25°
10K
R7
R8
R9
R10
R12
R13
No Load
I2C EEPROM
U1
A0
A1
A2
GND
VCC
WP
SCL
SDA
24LC02B-I/SN
Do Not Populate
J1
NC
LED4
LED5
LED7
SHLD
22x2_RA_Header
CSSH1
CapSense Shield Electrode
SHLD
Shield
J2
HEADER 3
Default Jumper
Pin 2 and 3
NOTE:
- All LEDs are Reverse Mount
- All SMD components placed on back- (non-sensor) side of PCB to allow for overlays
PCB: PDC-9542
PCA: 121-54200
CYPRESS SEMICONDUCTOR © 2009
Title
CY3280-BMM CapSense Matrix Button Module
Size
B
Document Number
REF-15042
Rev
**
Date:
Monday, May 18, 2009
Sheet
1
of
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